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graph TD
    1[FACSIMILE TERMINAL] --- 3((PSTN))
    2[FACSIMILE TERMINAL] --- 3
    3 --- 4[PSTN SWITCHING SYSTEM]
    4 --- 5[FACSIMILE MAIL BOX SERVICE SUBSCRIBER APPARATUS]
  
```

The block diagram illustrates the architecture of a facsimile system. The components and their interconnections are as follows:

- CONTROL BOARD**: A bracketed label on the left side of the diagram, encompassing the **CONTROL BOARD INFORMATION EXCHANGE MEMORY** (19) and the **FACSIMILE DATA STORING UNIT CONNECTION UNIT** (12).
- CONTROL BOARD INFORMATION EXCHANGE MEMORY** (19): A rectangular block connected to the CPU (11) and the FACSIMILE DATA STORING UNIT CONNECTION UNIT (12).
- CPU** (11): A rectangular block connected to the CONTROL BOARD INFORMATION EXCHANGE MEMORY (19) and the FACSIMILE DATA STORING UNIT CONNECTION UNIT (12).
- FACSIMILE DATA STORING UNIT CONNECTION UNIT** (12): A central rectangular block that receives data from the CONTROL BOARD and the CPU, and routes it to the BUFFER MEMORY (13), the PCM HIGHWAY MATCHING UNIT (14), and the SYNCHRONOUS PROCESSOR (18).
- BUFFER MEMORY** (13): A rectangular block connected to the FACSIMILE DATA STORING UNIT CONNECTION UNIT (12) and the PCM HIGHWAY MATCHING UNIT (14).
- PCM HIGHWAY MATCHING UNIT** (14): A rectangular block that receives data from the BUFFER MEMORY (13) and the FACSIMILE DATA STORING UNIT CONNECTION UNIT (12). It has two external ports: **Tx** (transmit) and **Rx** (receive).
- FACSIMILE CONTROLLER** (15): A rectangular block connected to the FACSIMILE DATA STORING UNIT CONNECTION UNIT (12), the MEMORY (16), the FACSIMILE MODEM (17), and the SYNCHRONOUS PROCESSOR (18).
- MEMORY** (16): A rectangular block connected to the FACSIMILE CONTROLLER (15).
- FACSIMILE MODEM** (17): A rectangular block connected to the FACSIMILE CONTROLLER (15) and the SYNCHRONOUS PROCESSOR (18).
- SYNCHRONOUS PROCESSOR** (18): A rectangular block connected to the FACSIMILE DATA STORING UNIT CONNECTION UNIT (12), the FACSIMILE CONTROLLER (15), and the FACSIMILE MODEM (17).



FIG. 1
BACKGROUND ART

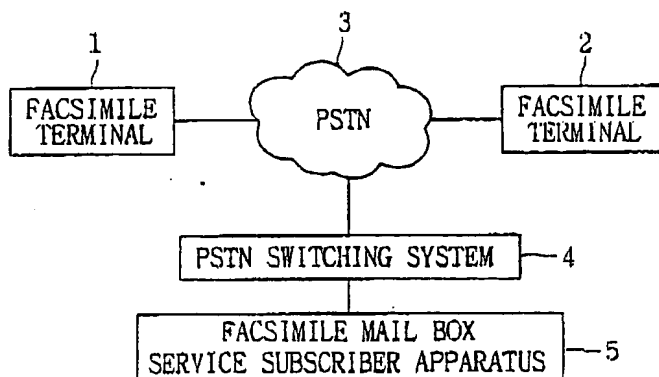


FIG. 2
BACKGROUND ART

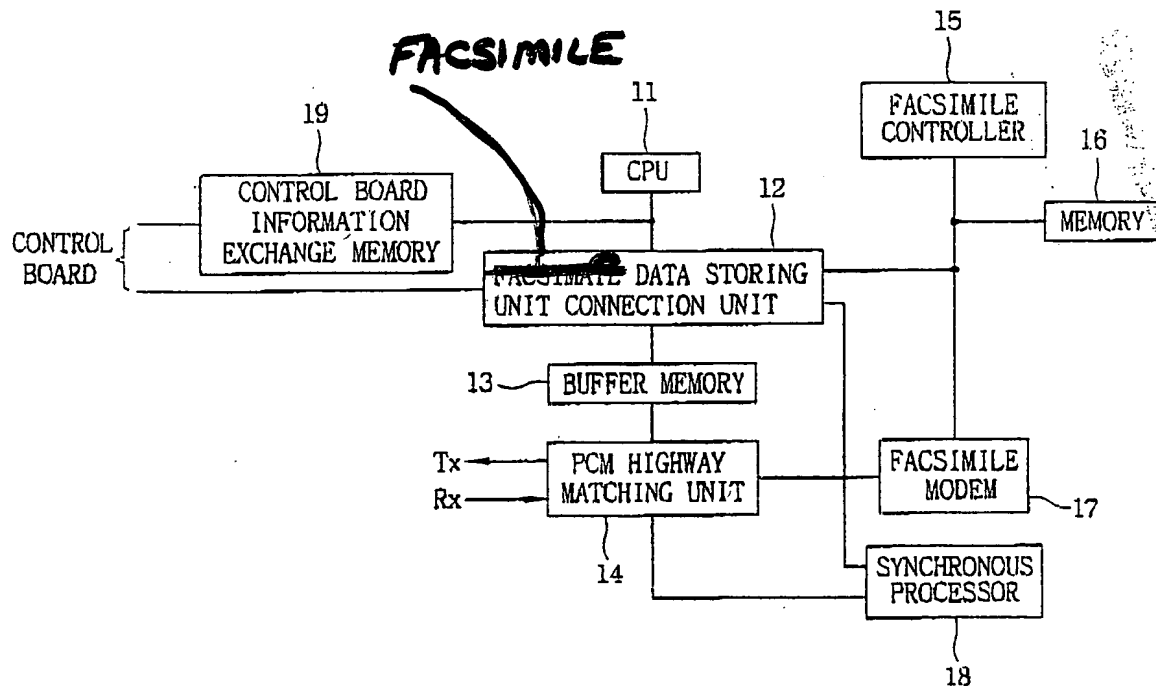




FIG. 3

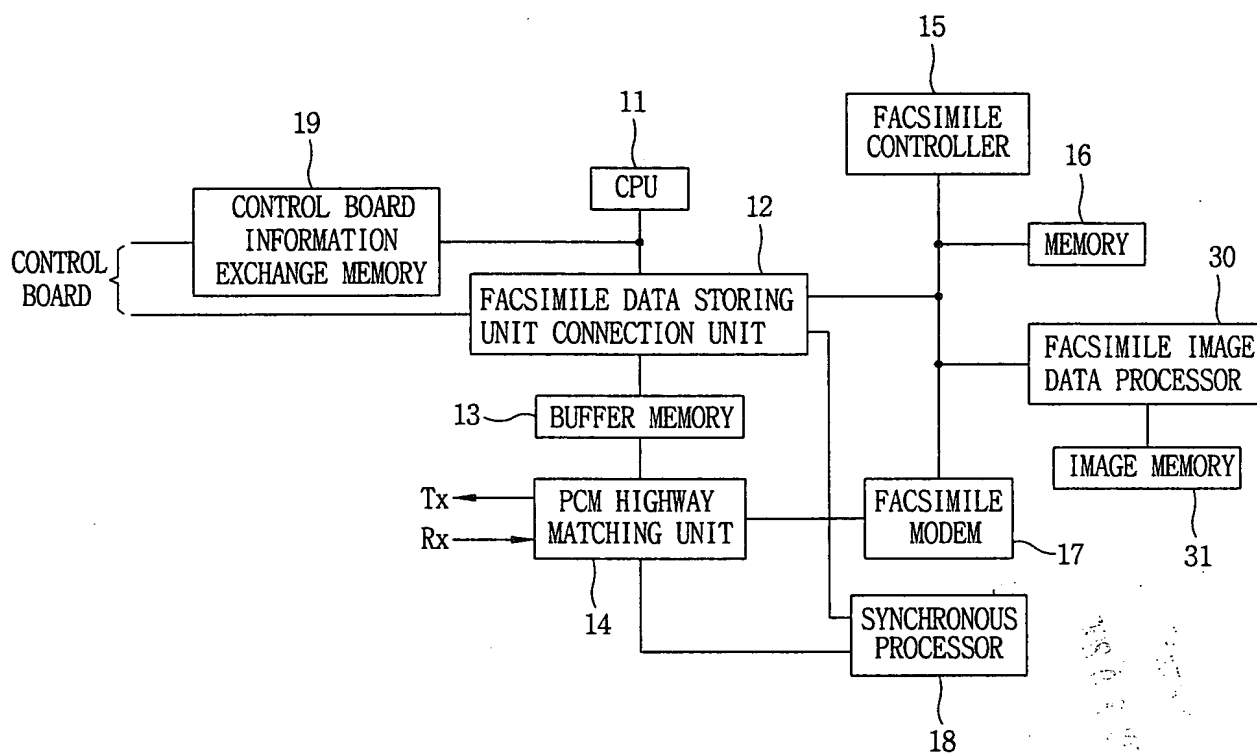




FIG. 3

